



XMC to PCIe Carrier

The accessory supporting PCIe installations for flight grade VITA 42 XMC modules



XMC to PCIe carrier allows the use of single-width XMC modules with VITA 61 (XMC 2.0) style connectors in four-lane PCI Express Gen 3 motherboard edge card slots. It supports Ethernet connectivity via three RJ-45 connectors accessed through the PCIe card front bracket, or alternatively through the rear VME connector. Additional JTAG, IPMI I2C and VITA discretes interfaces are available for user debugging and control.

Product Description

XMC to PCIe carrier is a passive device designed to host XMC form factor mezzanine card ^{TTE}End System A664 Lab (XMC) v2.0 and other compatible designs for laboratory applications and early integration phase in aviation projects inside an air-cooled chassis.

XMC to PCIe carrier is compatible with PCIe 3.0 x4 slots. PCI express lanes on the J15 XMC connector receptacle are routed to the male PCI express edge finger connector. Ethernet interface on the J16 XMC connector receptacle is routed to 3 front I/O channels Ethernet interface (RJ-45 connectors).

The rear 96 pin VME connector provides alternative access to the Ethernet physical interface and GPIO signals from J16.

XMC to PCIe carrier accepts +3.3V power supply on the PCIe connector for destination to the XMC module through J15 connector.

The XMC To PCIe Carrier features eight active LEDs to visually indicate key VITA 42 signals and voltage status. Alternatively, DIP switches can toggle the state of their associated LED.



Application Fields

- Technology evaluation
- Product testing
- Architecture development



Carrier Capabilities	 Pluggable PCle 3.0 x4 carrier for XMC modules Hosting capabilities: VITA 42.0-2021 mezzanine single slot card Compatible with SOSA™ aligned XMC host cards with profile P1w9-X12d+P2w3-X38s+X8d per ANSI/VITA 46.9-2018 Activity LEDs for the following signals: +3V3 +3V3 AUX +12V PERST# / MRSTI# MRSTO# MPRESENT# MBIST# MVMRO
Interfaces	 Ethernet: RJ-45 x3 connectors for data traffic PCIe: x4 (64 pins) standard PCIe edge connector VITA 61 XMC connector receptacles J15 & J16 VITA discretes: 16-pin 2x8 header for debugging and control JTAG: standard 14-pin 2x7 header compatible with Xilinx Platform Cable USB II IPMI I²C: standard 10-pin 2x5 header VME: 96 pin connector for GPIO signal access, optional Ethernet data traffic
Supported Standards	 PCI Express® Base Specification Rev 3.0 PCI Express® Card Electromechanical specification Rev 3.0 ANSI/VITA 42.0-2021 ANSI/VITA 61.0-2014
Dimensions	– 201 x 123 x 22 (in mm)
Weight	– 130g
Power Supply (on PCIe connector)	 +3.3V with destination on J15 Up to 25 W power supply through the PCIe edge connector
Power Consumption	 0.2W (passive carrier)
Environmental Operating Range	 Operating Temperature: 0°C to 55°C Storage Temperature: -40°C to 85°C Relative Humidity: 25-90%
Manufacturing & Environmental Regulations	- CE & FCC compliance
Documentation	– User Manual
Order Number	- 14575
Compatible products	 13434 TTE-End System A664 Lab (XMC) v2.0 14497 TTE-End System A664 Pro (XMC) v2.0 – Flight Product Representative non-coated 14240 TTE-End System A664 Pro (XMC) v2.0 – Flight Product Representative coated



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